

APPROVED	O.G. FIG.
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BY	
DRAFTSMAN	

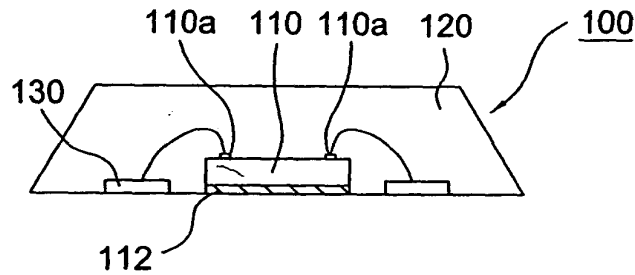


FIG. 1(PRIOR ART)

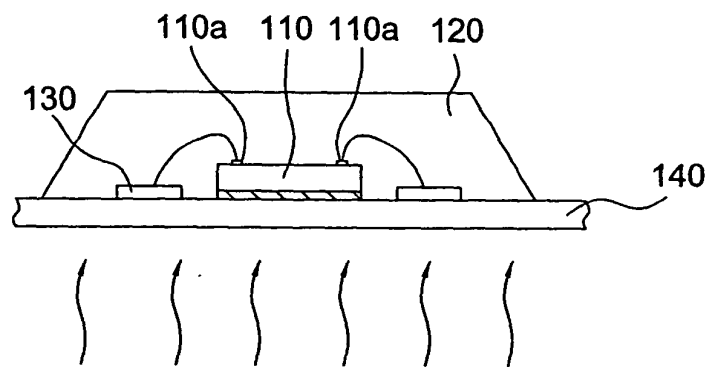


FIG. 2(PRIOR ART)

FIG. 1(PRIOR ART)

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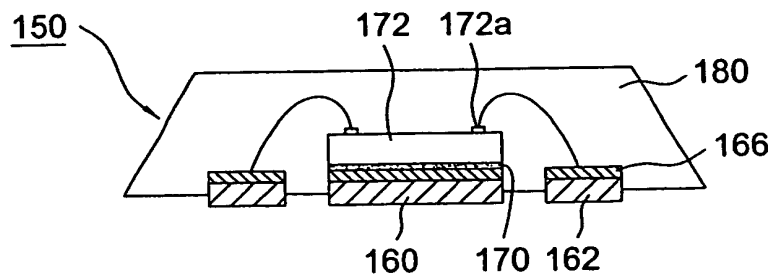


FIG. 3(PRIOR ART)

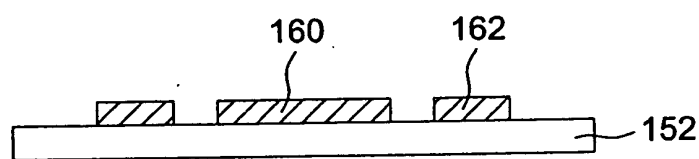


FIG. 4(PRIOR ART)

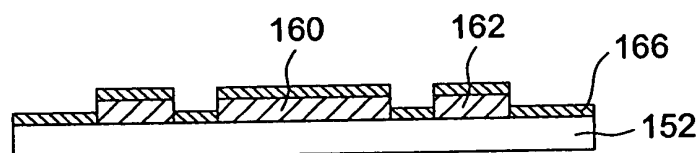


FIG. 5(PRIOR ART)

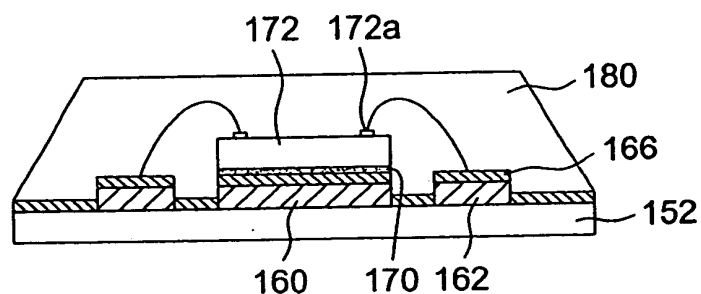


FIG. 6(PRIOR ART)

LOW-PIN-COUNT CHIP PACKAGE AND MANUFACTURING METHOD THEREOF

Divisional of S.N. 09/516,025

Inventor: Chun Hung LIN

Docket No. 4459-018A

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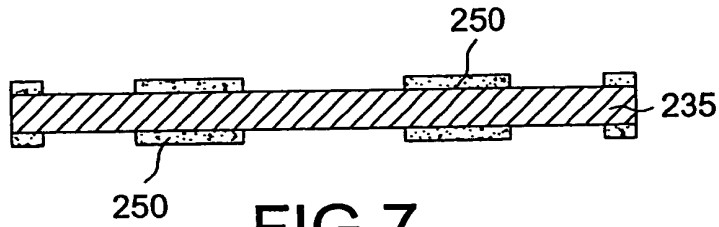


FIG. 7

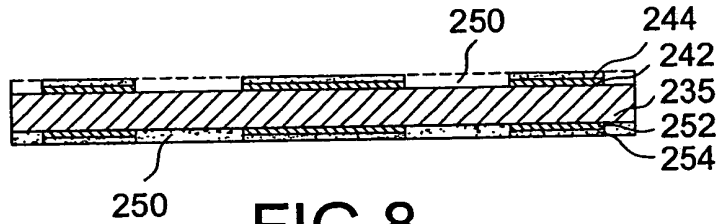


FIG. 8

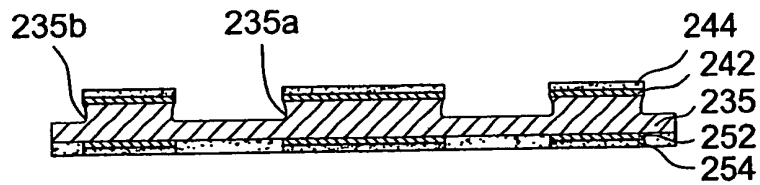


FIG. 9

FIG. 7

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[illegible]

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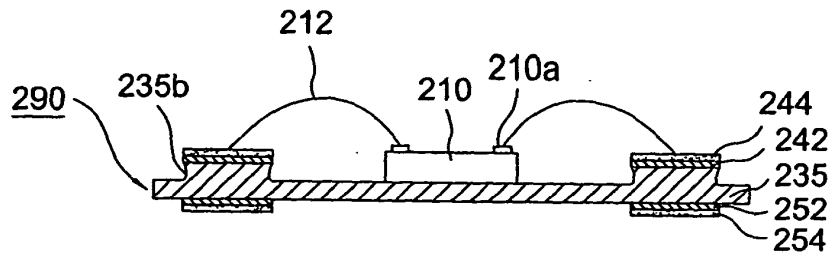


FIG.13

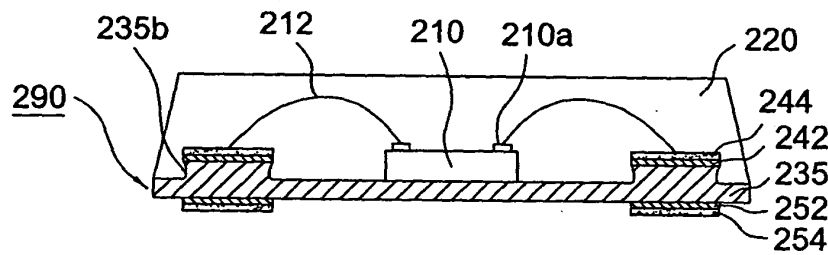


FIG.14

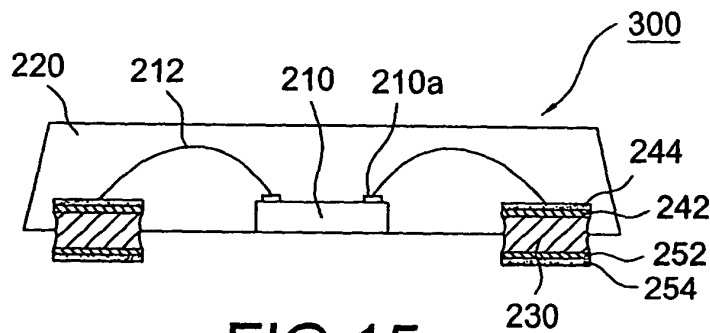


FIG.15

FIG. 13

LOW-PIN-COUNT CHIP PACKAGE AND MANUFACTURING METHOD THEREOF

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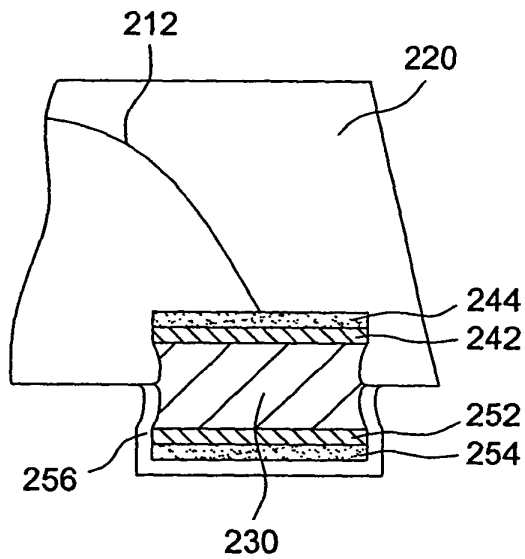


FIG.16

Patent Attorney